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PATENT NUMBER

U.S. UTILITY Patent Application

<p><i>Bo</i> O.I.P.E.</p> <p><i>3</i></p> <p>SCANNED <i>SS</i> Q.A. <i>1c</i></p>	PATENT DATE
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APPLICATION NO. 09/843630	CONT/PRIOR F	CLASS 438 257	SUBCLASS 787	ART UNIT 2842 2827	EXAMINER Mitchell
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Kazuo Nishiyama

Chip-like electronic components, a method of manufacturing the same, a pseudo wafer therefor and a method of manufacturing thereof

PTO-2040
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig. 1K</div> </div>		CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
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